INFORMATION DISCLOSURE OFFICE 1449	Atty. Docket No. 042012	Serial No. 10/754,577
	Applicant(s): Hiroshi KUDO et al.	
	Filing Date: January 12, 2004	Group Art Unit: 2822

JUL 1 3 2004

## U.S. PATENT DOCUMENTS

Examiner &	<u>"                                     </u>	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
Initial OF	AA						
	AB						
	AC						
	AD			<del> </del>			
	AE					<u></u>	

FOREIGN PATENT DOCUMENTS

		r	UKEIGN PAI	ENI DUC	
Examiner Initial		Document No.	Date	Country	Translation (Yes or No)
CAL	AF	11-251595	09/17/1999	Japan	Yes (Abstract Only), see page 2 in the spec.
(AL	AG	11-261063	09/24/1999	Japan .	Yes (Abstract Only), see page 2 in the spec.
CAL	AH	2001-024187	01/26/2001	Japan	Yes (Abstract Only), see page 2 in the spec.
CAL	AI	2001-274379	10/05/2001	Japan	Yes (Abstract Only), see pages 2 and 52 in the spec.
CAL	AJ	10-125677	05/15/1998	Japan	Yes (Abstract Only)

## OTHER DOCUMENTS

CAL	AK	Murarka, S. P., "Silicides for VLSI Applications", Academic Press, Inc., pp. 88~95, 1983, (see pages 3 and 44 in the spec.)			
CAL	AL	Qin M. et al., "Investigation of Polycrystalline Nickel Silicide films as a Gate Material", Journal of The Electrochemical Society, 148 (5) (The Electrochemical Society, Inc.), pp. G271~G274 (2001) (see pages 3 and 49 in the spec.)			
Examiner		Date Considered -2/16/2005			